

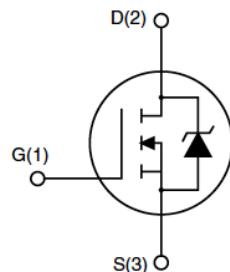
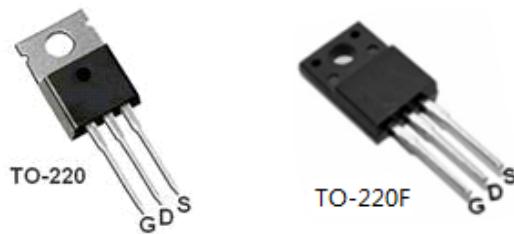
**800V N-Channel MOSFET****FEATURES**

VDSS	RDS ON @ 10V (Typ)	ID
800V	2.3 Ω	5 A

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- RoHS Compliant

**APPLICATIONS**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

**Schematic diagram****Ordering Information**

Part Number	Marking	Case	Packaging
G5N80T	G5N80	TO-220	50pcs/Tube
G5N80F	G5N80	TO-220F	50pcs/Tube

**Absolute Maximum Ratings**  $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Value		Unit
		TO-220F	TO-220	
Drain-Source Voltage ( $V_{GS} = 0\text{V}$ )	$V_{DSS}$	800		V
Continuous Drain Current	$I_D$	5		A
Pulsed Drain Current (note1)	$I_{DM}$	20		A
Gate-Source Voltage	$V_{GSS}$	$\pm 30$		V
Single Pulse Avalanche Energy (note2)	$E_{AS}$	151		mJ
Avalanche Current (note1)	$I_{AS}$	5.5		A
Repetitive Avalanche Energy (note1)	$E_{AR}$	90		mJ
Power Dissipation ( $T_C = 25^\circ\text{C}$ )	$P_D$	25	70	W
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~+150		°C

**Thermal Resistance**

Parameter	Symbol	Value		Unit
		TO-220F	TO-220	
Thermal Resistance, Junction-to-Case	$R_{thJC}$	5	1.78	K/W
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62.5	60	

**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

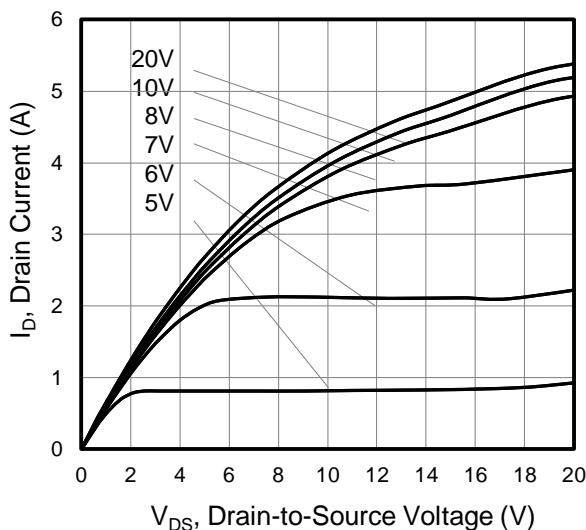
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	800	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 800\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 25^\circ\text{C}$	--	--	1	$\mu\text{A}$
		$V_{\text{DS}} = 640\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$	--	--	100	
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 30\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	3.0	--	4.0	V
Drain-Source On-Resistance (Note3)	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 2.5\text{A}$	--	2.3	2.8	$\Omega$
<b>Dynamic</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1.0\text{MHz}$	--	667	--	pF
Output Capacitance	$C_{\text{oss}}$		--	77	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	14	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = 640\text{V}, I_D = 5\text{A}, V_{\text{GS}} = 10\text{V}$	--	27	--	nC
Gate-Source Charge	$Q_{\text{gs}}$		--	3.5	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	13	--	
Turn-on Delay Time	$t_{d(\text{on})}$	$V_{\text{DD}} = 400\text{V}, I_D = 5\text{A}, R_G = 25\Omega$	--	37	--	ns
Turn-on Rise Time	$t_r$		--	15	--	
Turn-off Delay Time	$t_{d(\text{off})}$		--	144	--	
Turn-off Fall Time	$t_f$		--	41	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	5	A
Pulsed Diode Forward Current	$I_{\text{SM}}$		--	--	20	
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 2.5\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.4	V
Reverse Recovery Time	$t_{rr}$	$V_{\text{GS}} = 0\text{V}, I_S = 5\text{A}, di/dt = 100\text{A}/\mu\text{s}$	--	1099	--	ns
Reverse Recovery Charge	$Q_{rr}$		--	3.2	--	$\mu\text{C}$

**Notes**

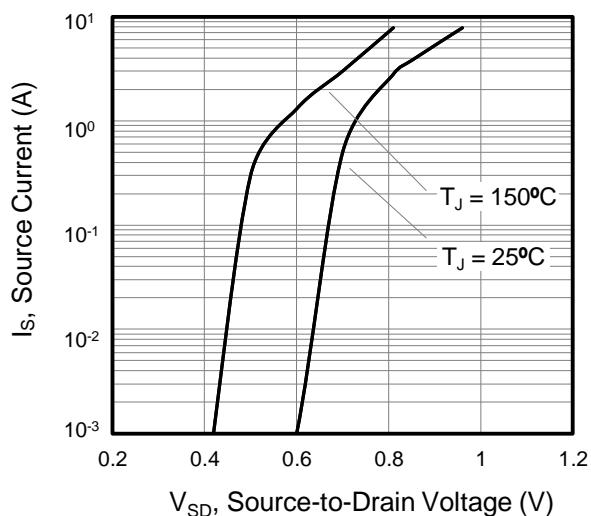
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2.  $I_{AS} = 3\text{A}, V_{DD} = 50\text{V}, R_G = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$
3. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 1\%$

**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

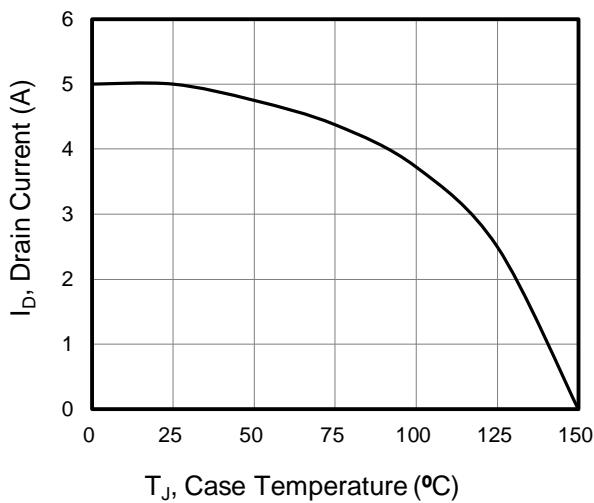
**Figure 1. Output Characteristics ( $T_J = 25^\circ\text{C}$ )**



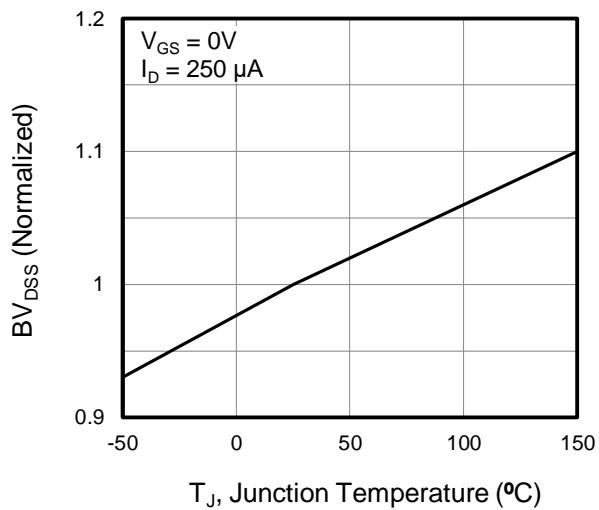
**Figure 2. Body Diode Forward Voltage**



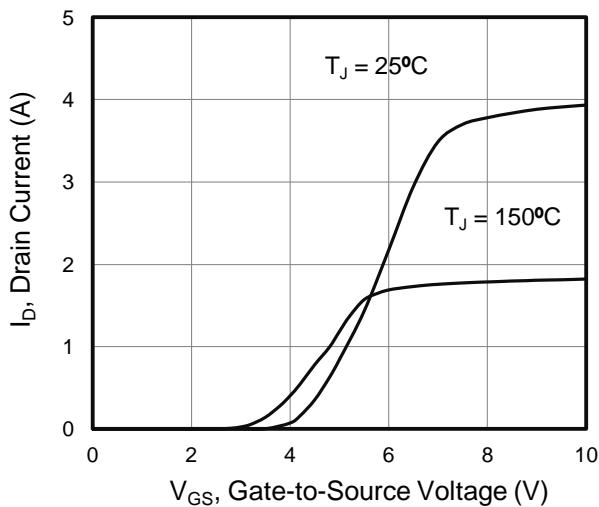
**Figure 3. Drain Current vs. Temperature**



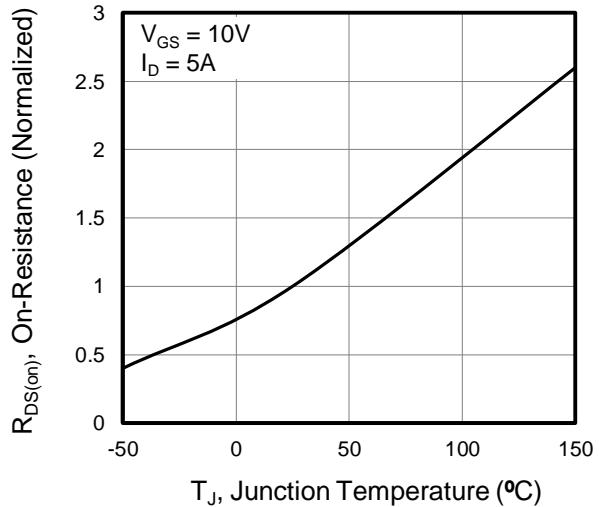
**Figure 4. BV<sub>DSS</sub> Variation vs. Temperature**



**Figure 5. Transfer Characteristics**

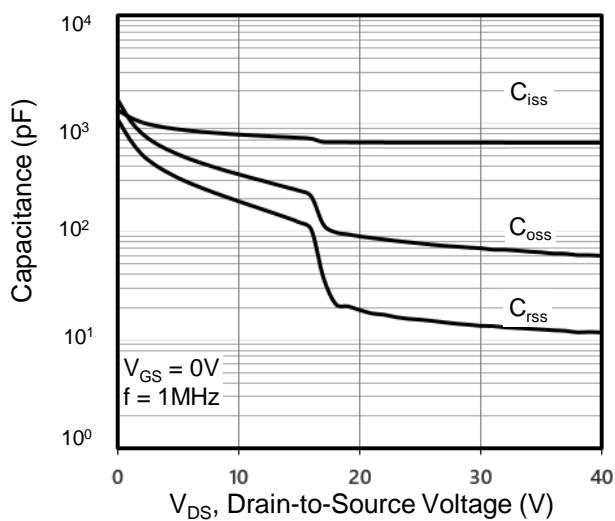


**Figure 6. On-Resistance vs. Temperature**

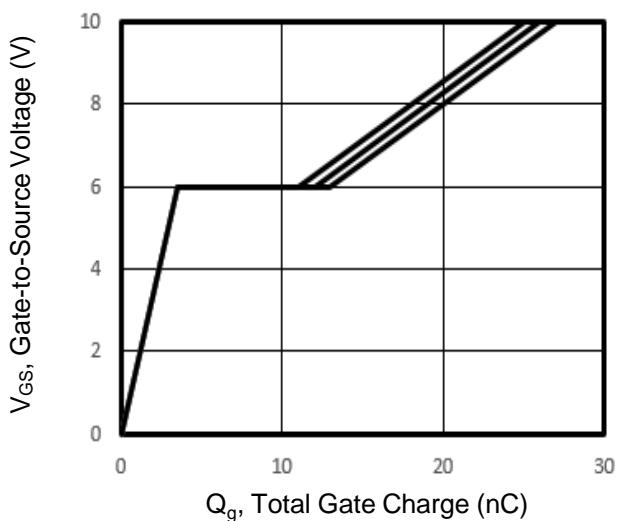


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

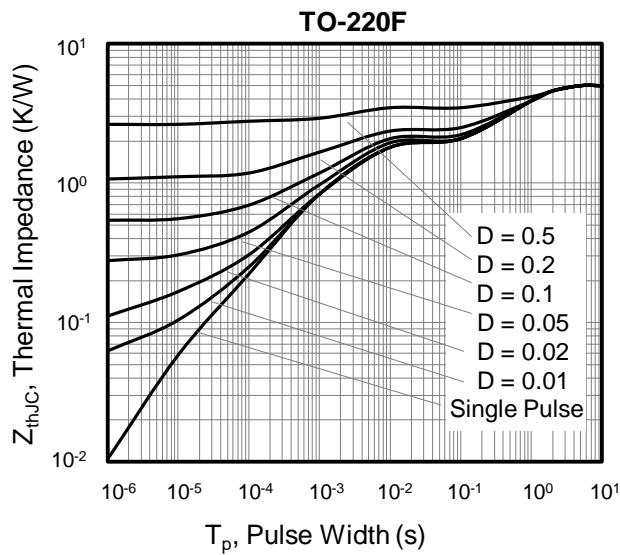
**Figure 7. Capacitance**



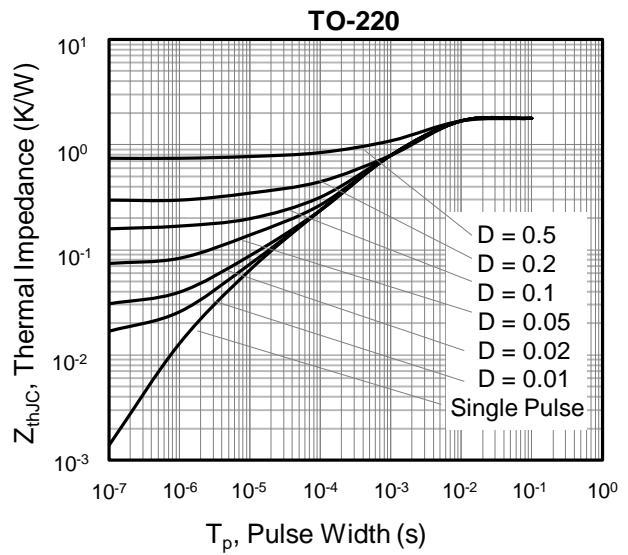
**Figure 8. Gate Charge**



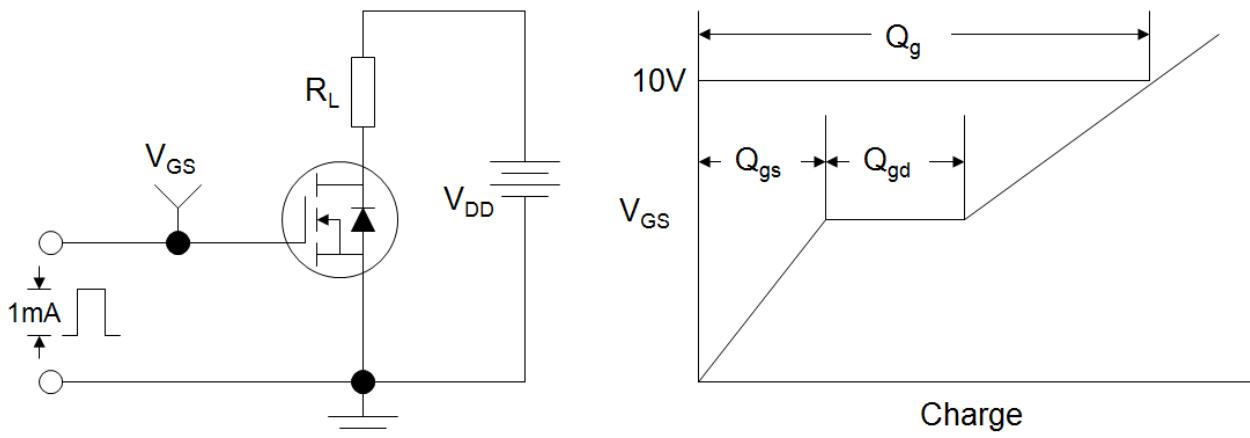
**Figure 9. Transient Thermal Impedance**



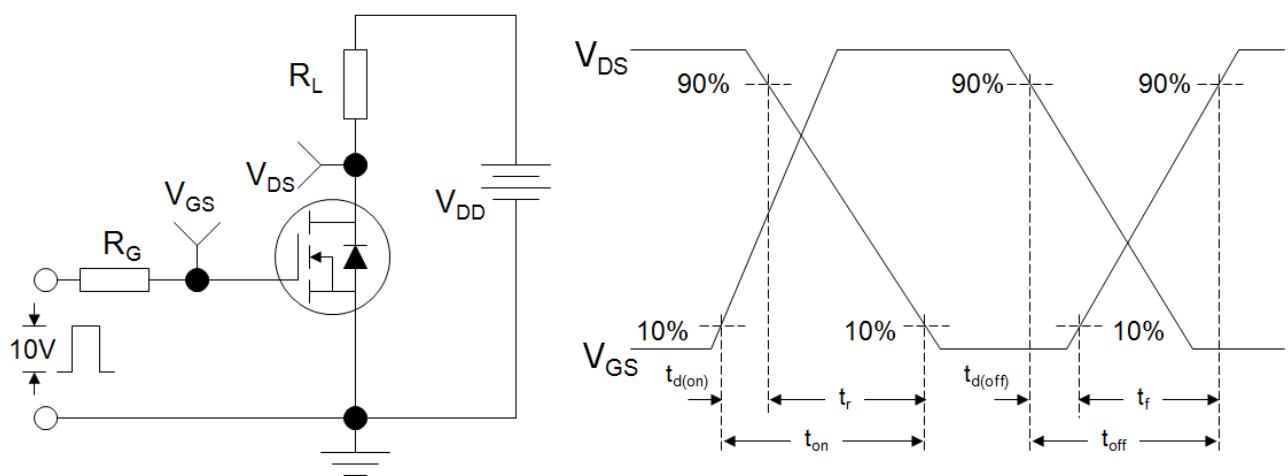
**Figure 10. Transient Thermal Impedance**



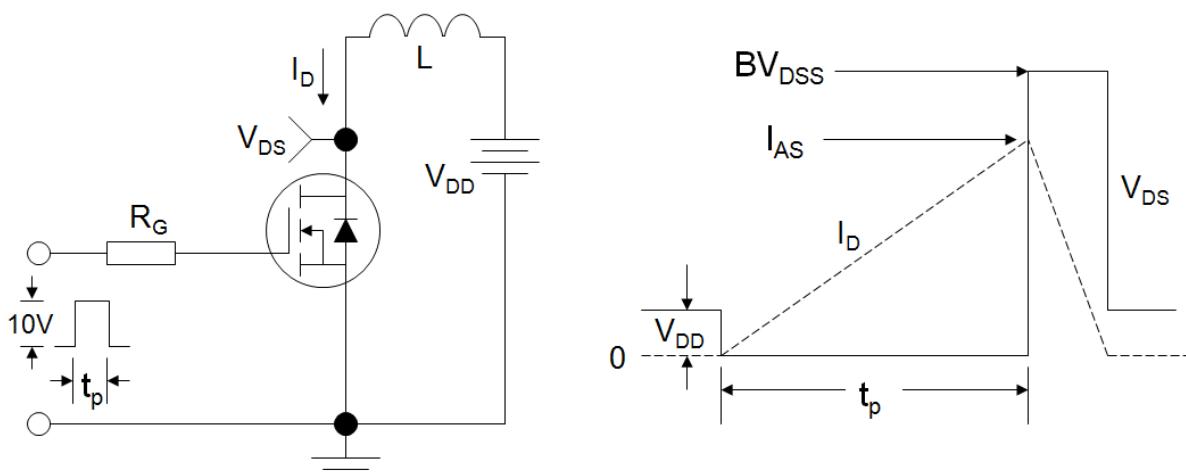
**Figure A: Gate Charge Test Circuit and Waveform**



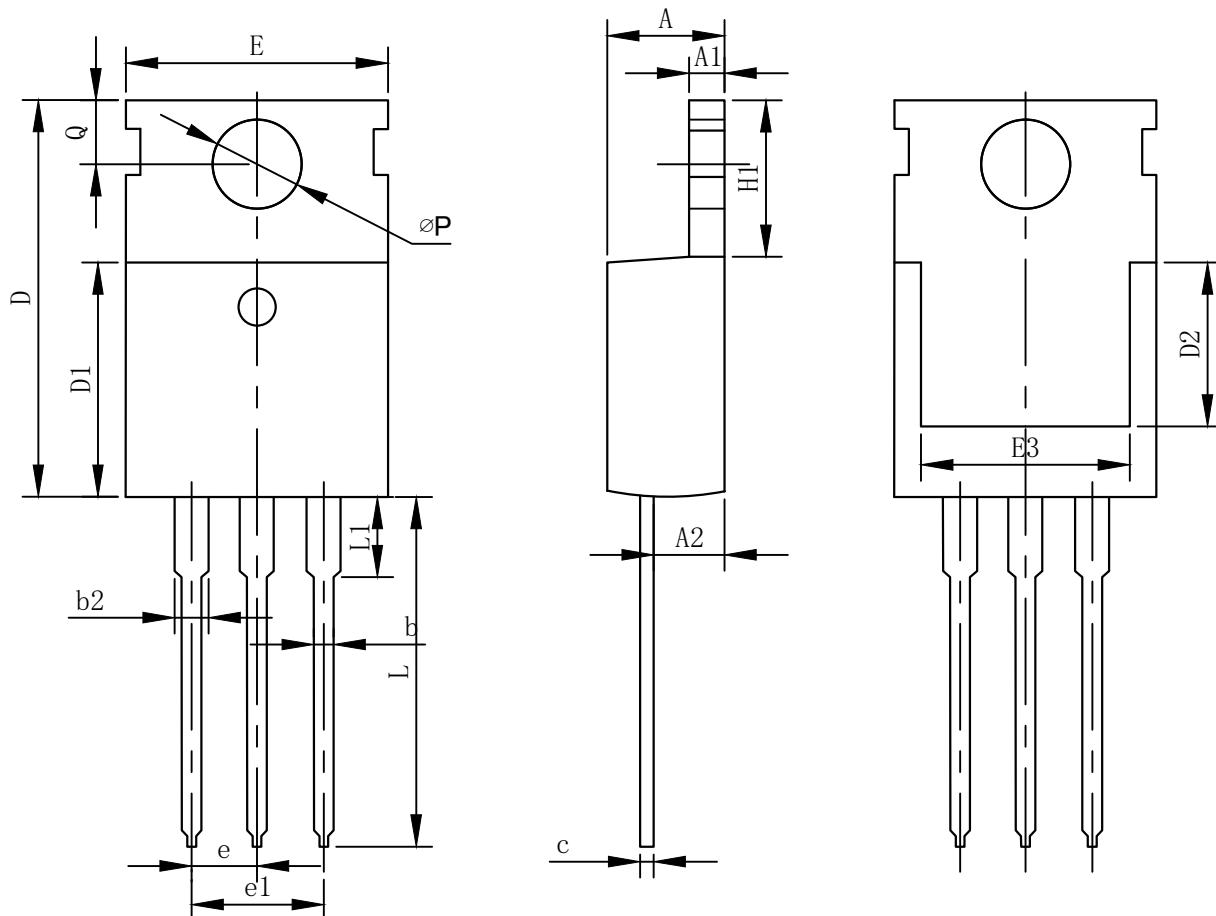
**Figure B: Resistive Switching Test Circuit and Waveform**



**Figure C: Unclamped Inductive Switching Test Circuit and Waveform**



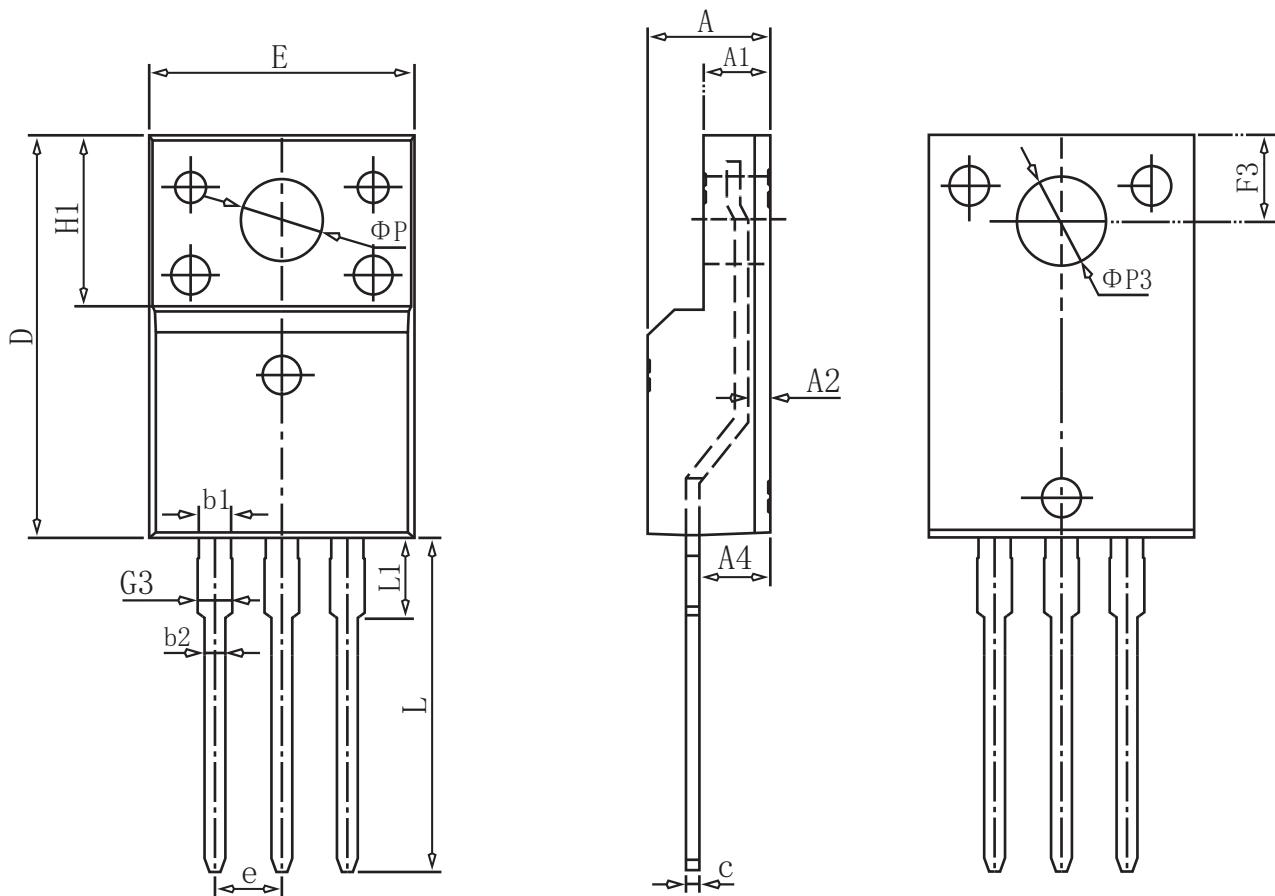
## TO-220 Package information



COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	4.37	4.57	4.70
A1	1.25	1.30	1.40
A2	2.20	2.40	2.60
b	0.70	0.80	0.95
b2	1.70	1.27	1.47
c	0.45	0.50	0.60
D	15.10	15.60	16.10
D1	8.80	9.10	9.40
D2	5.50	-	-
E	9.70	10.00	10.30
E3	7.00	-	-
e	2.54BSC		
e1	5.08BSC		
H1	6.25	6.50	6.85
L	12.75	13.50	13.80
L1	-	3.10	3.40
ØP	3.40	3.60	3.80
Q	2.60	2.80	3.00

## TO-220F Package information



## COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
E	10.00	10.20	10.40
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A2	0.65	0.85	1.30
A4	2.55	2.75	2.95
c	0.40	0.50	0.65
D	15.57	15.87	16.17
H1	6.70REF		
e	2.54BSC		
Φ P	3.183REF		
L	12.68	12.98	13.28
L1	3.25	3.45	3.65
Φ P3	3.45REF		
F3	3.10	3.30	3.50
G3	1.10	1.30	1.50
b1	1.05	1.20	1.35
b2	0.70	0.80	0.92